Texas Instruments reveals the 0.3-inch HD Tilt & Roll Pixel (TRP) DLP Pico chipset at Mobile World Congress 2014-- the smallest micro-mirror array from the company, able to generate 720p display from compact applications.

Designed for use in mobile devices, wearable displays, augmented reality displays, interactive surface computing, digital signage and control panels, the chipset leverages proprietary TRP DLP architecture and adaptive IntelliBright algorithms to deliver higher brightness and lower power consumptions than previous DLP Pico chipsets.

In addition fast switching speeds enable true colour RGB engines with 120Hz video performance.

"We are achieving x2 the number of pixels in a 0.3-inch MEMS device with 30% greater optical efficiency and up to 50% power savings on a frame-by-frame basis than our previous architectures," DLP Pico says. "This allows developers to create a wider variety of applications and products in smaller form factors than ever before."

One company already making use of the chipset is Avegant, whose Glyph virtual retinal display product beams images directly into the human eye. Further products carrying the technology will be available by end 2014.

Go Texas Instruments 0.3-inch HD TRP DLP Pico Chipset